Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**ANODE**

**CATHODE**

**2.5**

**LT1004C**

**.065”**

**.057”**

**Top Material: Al**

**Backside Material: SiNi**

**Bond Pad Size: .004” X .004”**

**Backside Potential: ANODE (V-)**

**Mask Ref: LT1004C**

**APPROVED BY: DK DIE SIZE .057” X .065” DATE: 3/21/19**

**MFG: TEXAS INSTRUMENTS THICKNESS .025” P/N: LT1004-2.5**

**DG 10.1.2**

#### Rev B, 7/19/02